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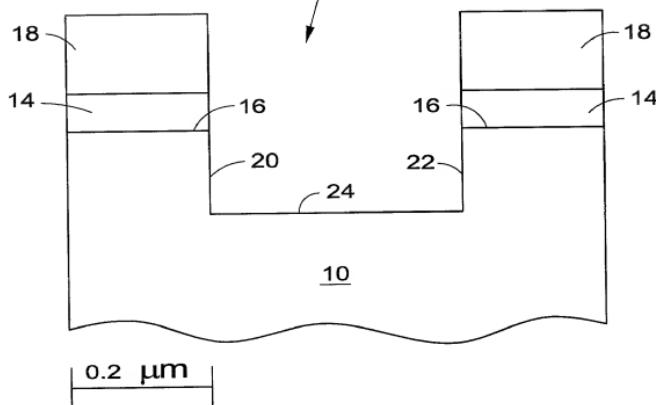


Fig. 1

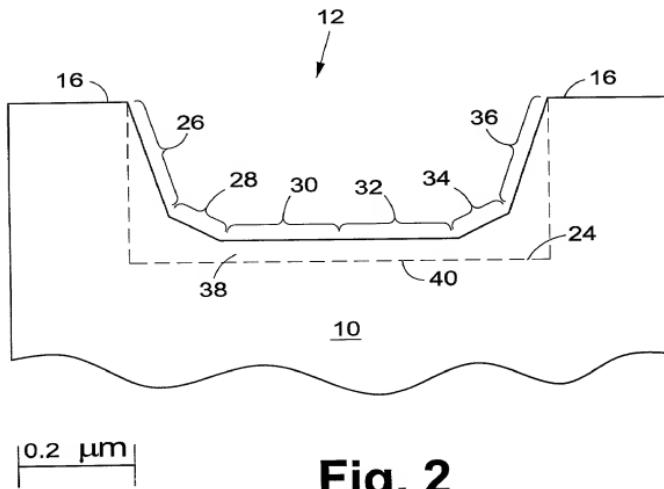


Fig. 2

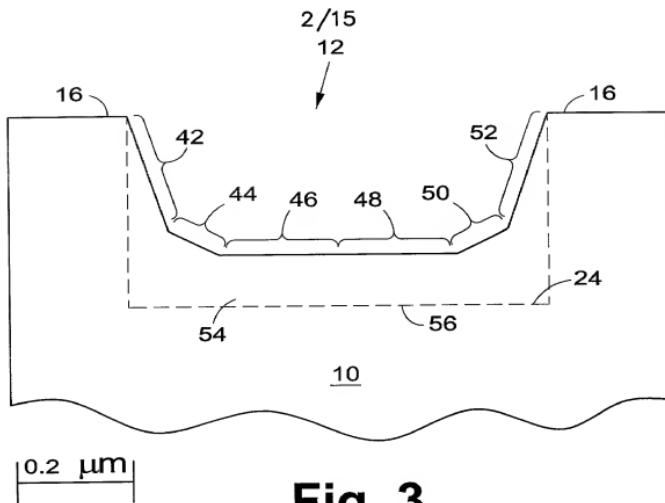


Fig. 3

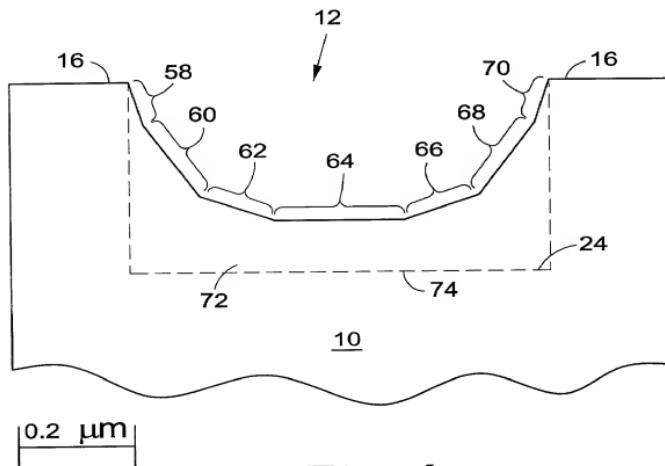


Fig. 4

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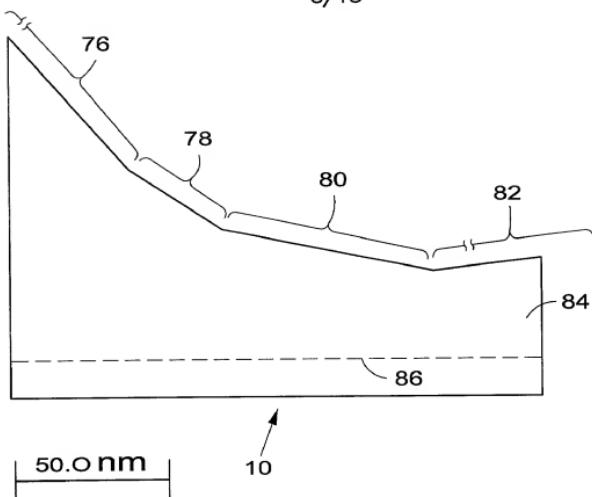


Fig. 5

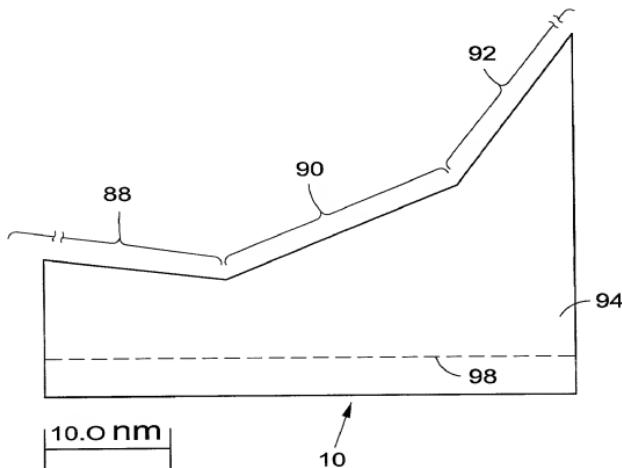


Fig. 6

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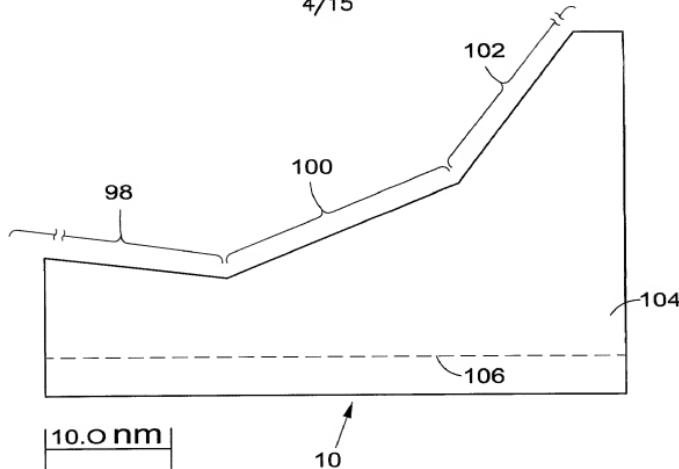


Fig. 7

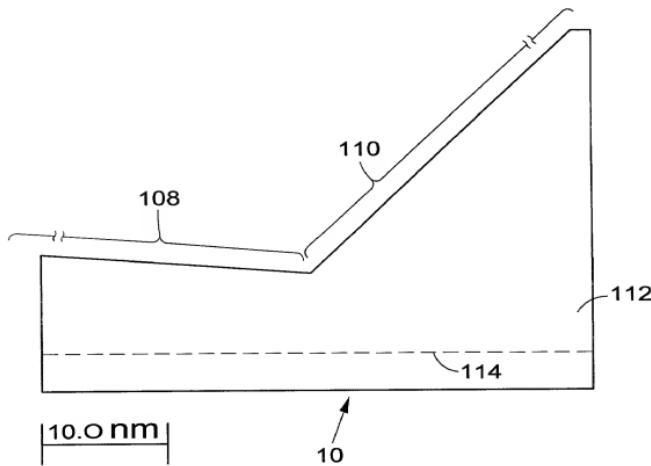


Fig. 8

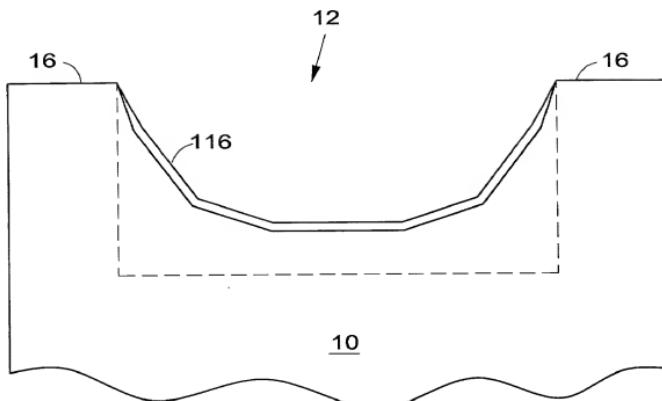


Fig. 9

TITLE: VERTICAL TRANSISTOR AND METHOD OF MAKING
INVENTOR'S NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

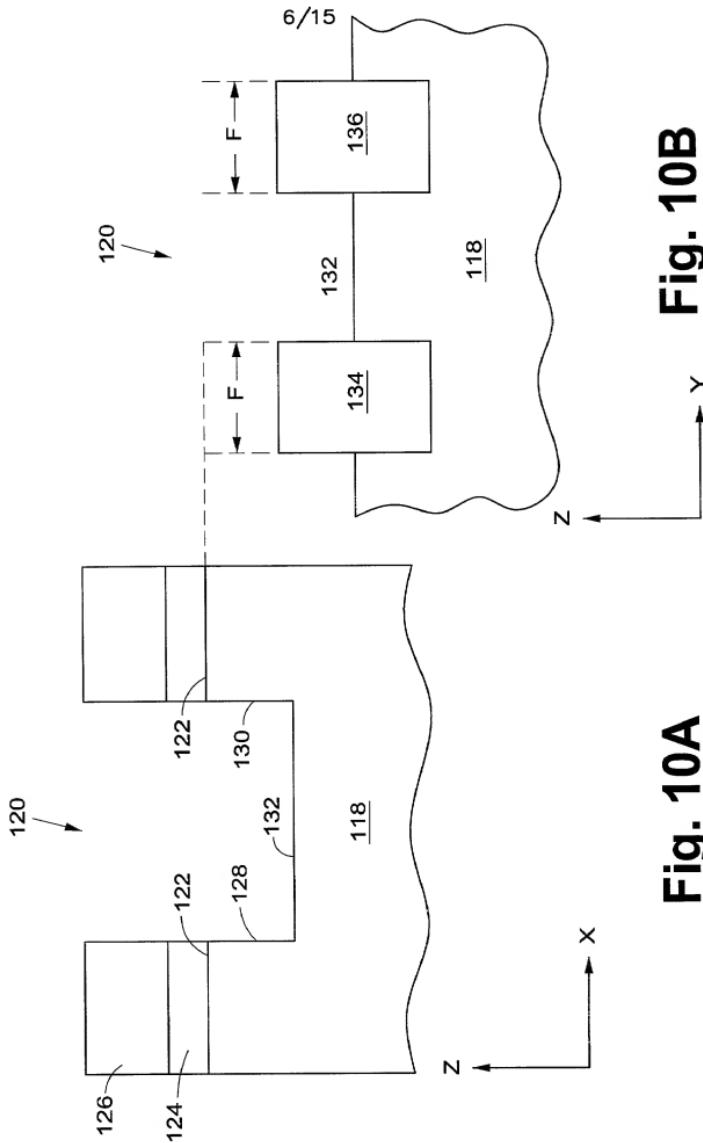


Fig. 10B

Fig. 10A

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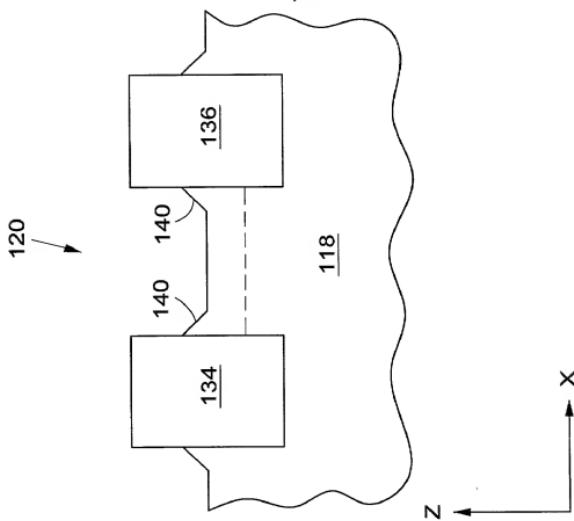


Fig. 11B

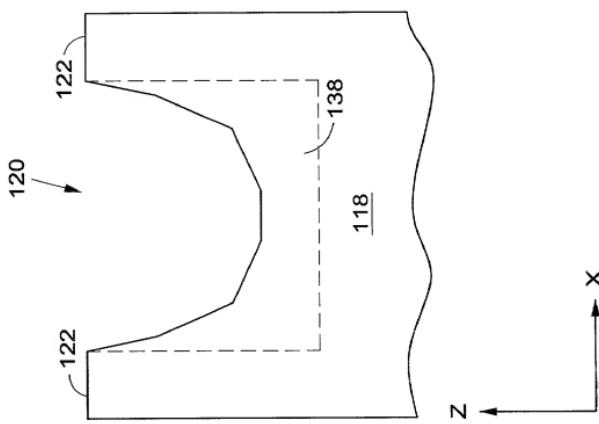


Fig. 11A

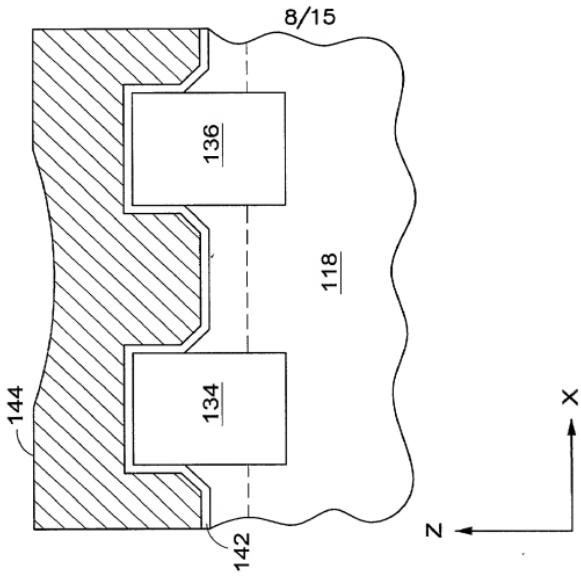


Fig. 12B

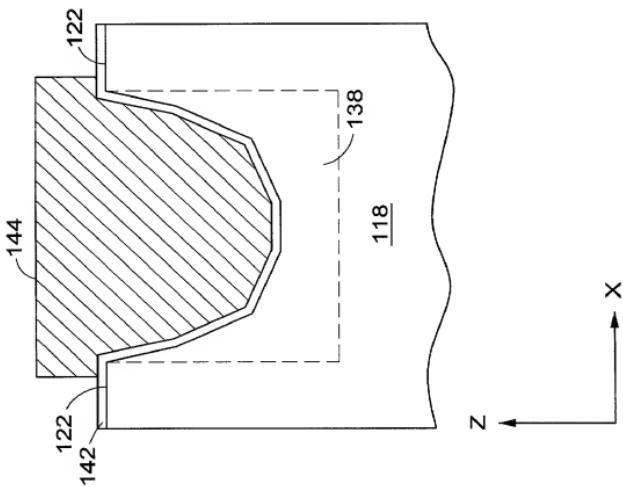


Fig. 12A

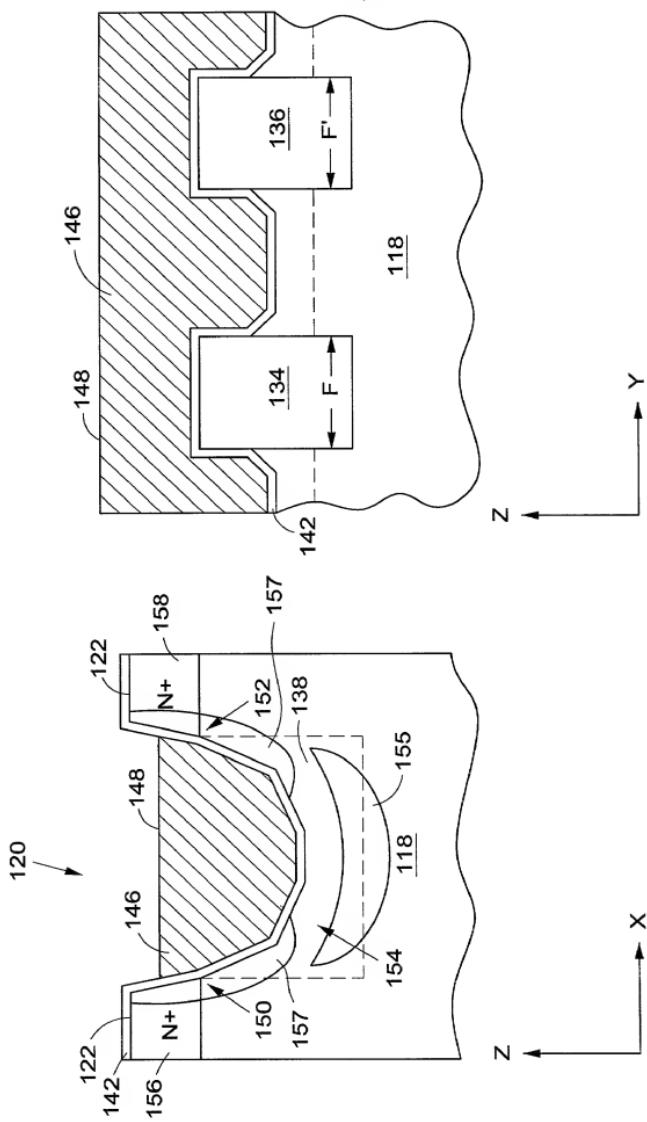


Fig. 13A

Fig. 13B

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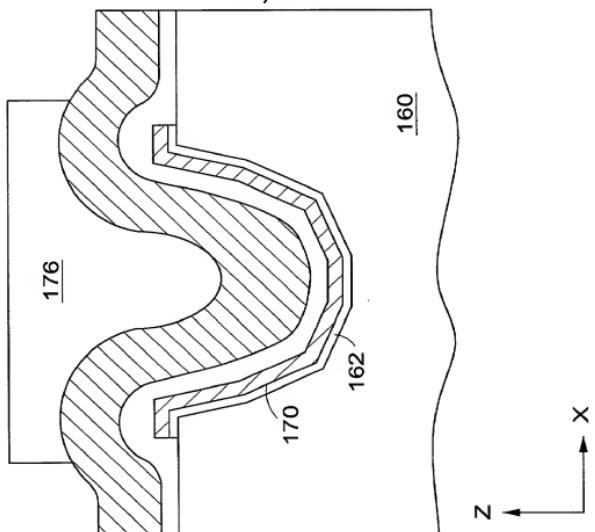


Fig. 15

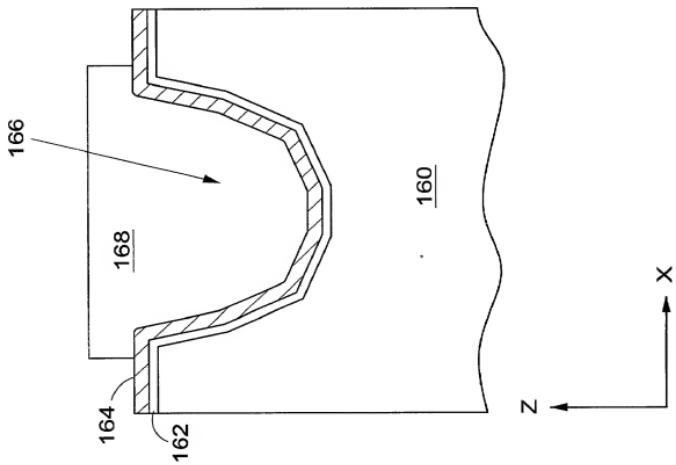


Fig. 14

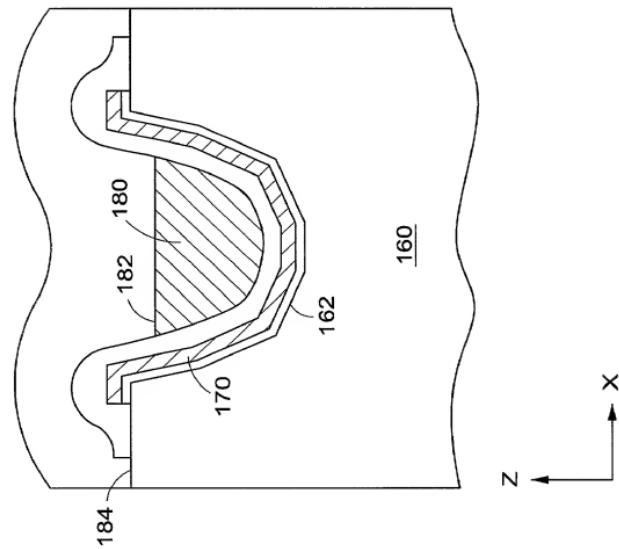


Fig. 17

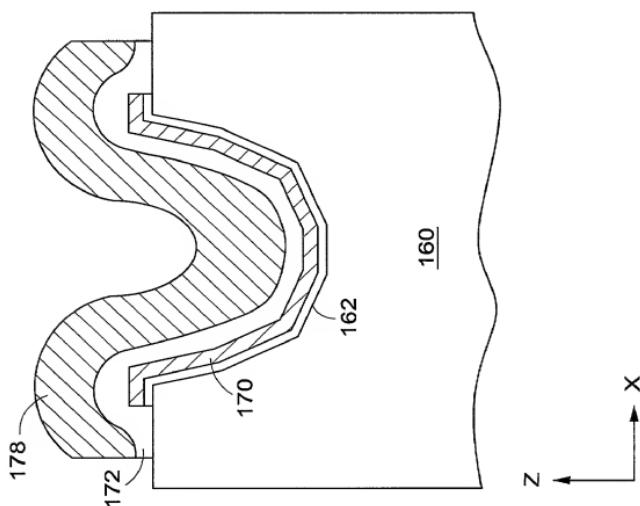


Fig. 16

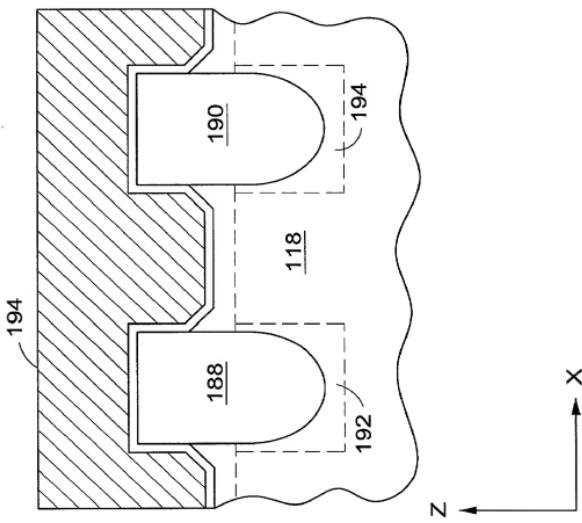


Fig. 18B

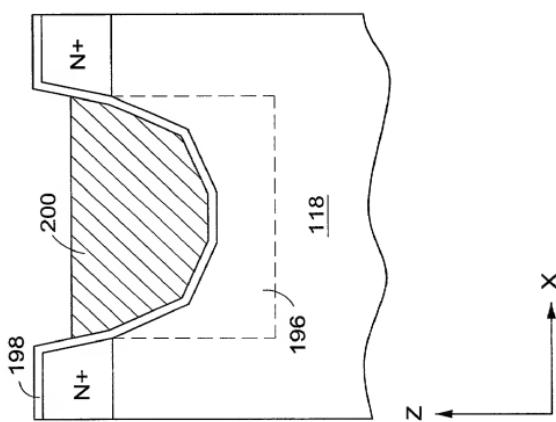


Fig. 18A

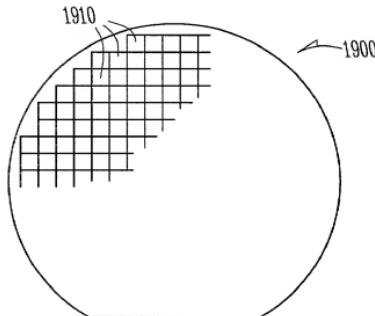


Fig. 19

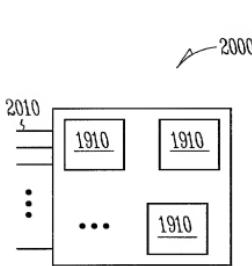


Fig. 20

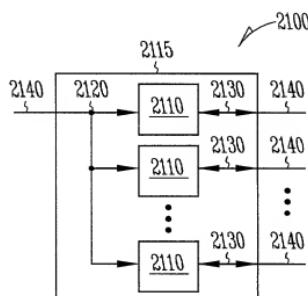


Fig. 21

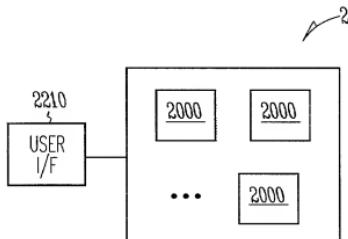


Fig. 22

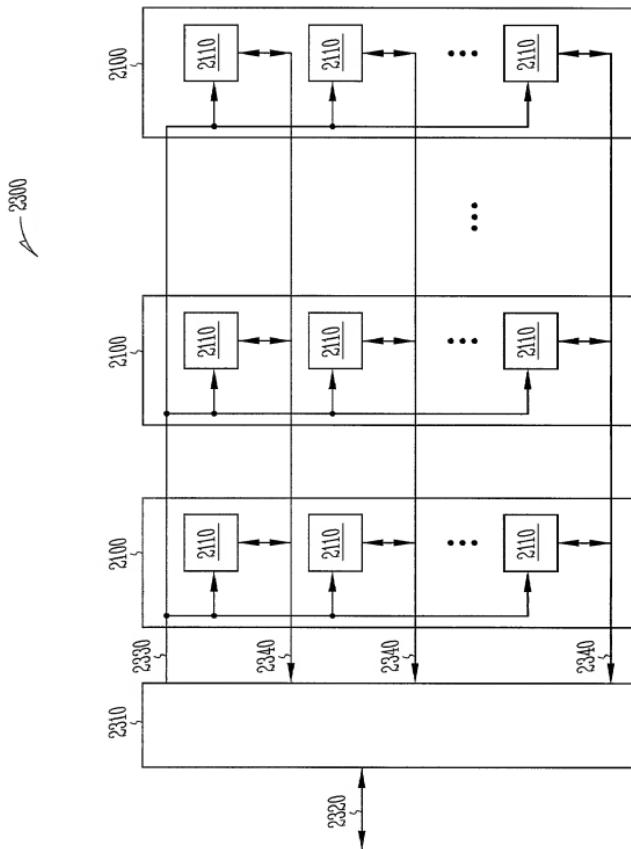


Fig. 23

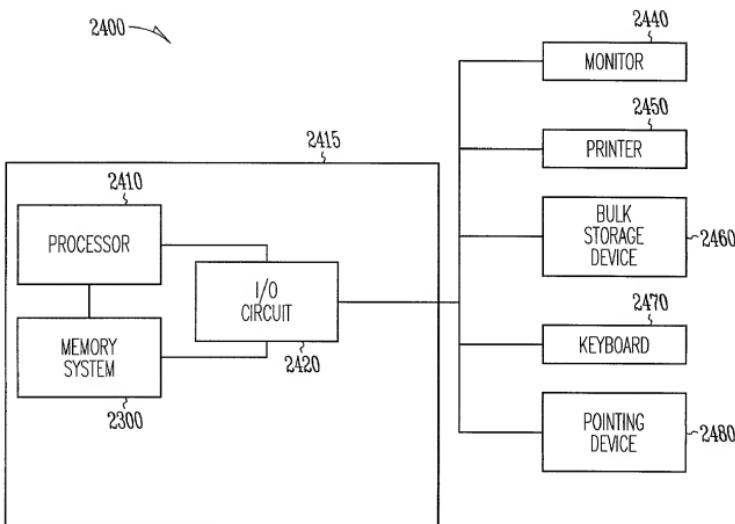


Fig. 24